**CSCI 4335 Computer Architecture**

**Dr. Abraham**

**Assignment 4 - Problems**

We learned that based on the architecture we can have 3 or more operand instructions, 2 operand instructions, 1 operand instructions and 0 operand instructions. This assignment is for you to write assembly code for the following problem using each of the architecture. Do not forget operator precedence and associativity of operations. For 0 operand instructions you need to convert to postfix first (PUSH AND POP WILL HAVE AN OPERAND). Assume all input variables are stored in RAM already. Please identify the type of architecture and how many operands are used. Each assembly line should follow with a comment like this (comment starts with a semicolon): **SUB R3,R2 ;R3 🡨 R3-R2.**

G =(A+B\*C)/(D-E \*F)